

APPENDIX 1**List of Confidential Witnesses**

CW	Title/Position	Dates of Employment
2	<p>Director of Finance at GlobalFoundries in the Malta, New York fabrication facility</p> <p>Responsible for starting up the Malta Fab by building up its finance organization. Heavily involved in working on the 2011 Wafer Supply Agreement with AMD.</p>	October 2009 through August 2012
3	<p>Engineer Manufacturing Operator at GlobalFoundries in the Malta, New York fabrication facility</p> <p>Responsible for driving production to meet scheduling demands on some of the Malta plant's production lines, including Llano.</p>	June 2011 through May 2012
4	<p>Controller of Technology and Alliances at GlobalFoundries</p> <p>Controller for worldwide research and development at GlobalFoundries.</p>	August 2011 through June 2013
5	<p>Senior Director, Silicon Design and Platform Engineering at AMD</p> <p>Worked on Electrical and Logical Validation, Debug, and Platform Silicon Infrastructure for all AMD CPUs, chipsets, and integrated video translation products. Also worked on the back end of all the issues that delayed Llano.</p>	September 1984 through March 2013
6	<p>Most recent title was Senior Member of Technical Staff at AMD</p> <p>Responsible for supporting AMD's engineering division.</p>	December 2004 through February 2014
7	<p>Most recent position (from approximately March 2010 through November 2011) was Director of Finance-Global Research and Development at AMD</p> <p>Worked at the Company's research and development organization.</p>	June 2007 through November 2011

CW	Title/Position	Dates of Employment
8	<p>Member of Technical Staff and Design Engineer at AMD</p> <p>Worked closely with the pre-silicon design and IP integration of Llano.</p>	May 2006 through December 2012
9	<p>R&D Specialist at Asus in Taiwan</p> <p>Responsible for rewriting the “CPU reference code of AMD’s Llano to enhance the algorithm for more power” and to enhance the performance of the motherboards for the retail market.</p>	January 2007 through April 2011